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PATENT

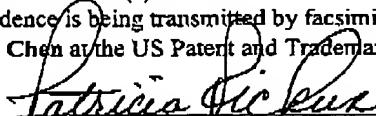
Attorney Docket No. MTI-31529

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Weimer, Ronald A
 Serial No. : 09/935,255
 Filing Date : August 22, 2001
 For : Method of Composite Gate Formation
 Group Art Unit : 2813
 Examiner : CHEN, Jack S.J.
 Confirmation No. : 1208

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being transmitted by facsimile to the central facsimile number (703) 872-9306 addressed to Examiner Chen at the US Patent and Trademark Office.

Date: 7-8-4

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, Applicant submits the information listed on the attached Form PTO-1449 for consideration in the above-identified application. No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103.

The references listed on the enclosed Form PTO-1449 were cited by the Examiner in a divisional application USSN 10/236,841. Accordingly, copies of these documents are not provided because these documents are available in the divisional file. However, if these copies are not available to the Examiner, Applicant will provide a copy upon request.

This Supplemental Information Disclosure Statement is being filed after the mailing of a first action on the merits and before the mailing of a final action under §1.113 (37 CFR §1.97(e)). Accordingly, please charge the required fee as set forth in §1.17(p) to Account No. 23-2053 for the consideration of these references.

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USSN 09/935,255

Supplemental Information Disclosure Statement

Return of the enclosed Form PTO-1449 in the next communication to Applicant, showing the citations as initialed and considered, is respectfully requested.

Respectfully submitted,



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Dated: July 8, 2004

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231		ATTY. DOCKET NO. MTI-31529	Serial No. 09/935,255
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant Ronald A. Weimer	Confirmation No. 1208
		Filing Date 08/22/2001	Group Art Unit 2813

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
	A1	5,563,093	10-08-96	Koda et al.	438	231
	A2	6,005,807	12-21-99	Chen	365	185.26
	A3	6,087,229	07-11-00	Aronowitz et al.	438	591
	A4	6,323,114 B1	11-27-01	Hattangady et al.	438	591

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial		Non-Patent Document
	C1	Wolf, Silicon Processing for the VLSI Era, Vol. 2-Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 332-335

Examiner Initials	Date Considered
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

MKE/952574.1

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